

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	285	dram with comprise with microprocessor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:45
L4	0	dram near2 comprise near2 microprocessor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:46
L5	1	(dram with comprise with microprocessor). clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:46
L6	1429	(711/105).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/06 13:47
L7	127	6 and (DRAM with microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:47
L8	2	6 and (DRAM with comprise with microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:48
L9	39	6 and (DRAM adj4 microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:48
L10	31	6 and (DRAM adj3 microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:48

L11	16	6 and (DRAM adj2 microprocessor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:48
L12	325	(711/101).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/06 13:54
L13	1	12 and (comprise adj2 (microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:55
L14	1	12 and (comprise adj3 (microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:55
L15	1	12 and (comprise adj4 (microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:56
L16	4941	(711/101-105).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2008/07/06 13:56
L17	25	16 and (comprise adj2 (microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:56
L18	13	16 and (comprise adj (microprocessor or microcontroller))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/07/06 13:56

7/6/2008 2:52:44 PM

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